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for Patents, Washington, D.C. 20231.



# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#### **Patent Application**

Applicant(s): Thomas J. Krutsick

Case:

10

Serial No.:

10/061,475

Filing Date:

February 1, 2002

Group:

2811

Examiner:

Steven Ho Yin Loke

Title:

Method of Fabricating Complementary

Self-Aligned Bipolar Transistors

# TRANSMITTAL LETTER

**Assistant Commissioner for Patents** Washington, D.C. 20231

Date: January 14, 2003

Sir:

Submitted herewith is the following document relating to the above-identified patent application:

(1) Response to Restriction Requirement.

There is no additional fee due in conjunction with the response. In the event of any nonpayment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762 as required to correct the error.

Respectfully submitted,

Wayne L. Ellenbogen

Attorney for Applicant(s)

Reg. No. 43,602

Ryan, Mason & Lewis, LLP

90 Forest Avenue

Locust Valley, NY 11560

(516) 759-7662

Date: January 14, 2003

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RESPONSE TO RESTRICTION REQUIREMENT

**Assistant Commissioner for Patents** Washington, D.C. 20231

Sir:

This is in response to the outstanding Office Action dated January 3, 2003 in the aboveidentified application. Claims 1-15 are currently pending in the application.

In the outstanding Office Action, the Examiner required Applicant to elect one of two different species identified by the Examiner, namely, Group I, drawn to a semiconductor device, and Group II, drawn to a method of fabricating a semiconductor device.

Applicant hereby elects without traverse the species of Group II for prosecution on the merits. Applicant submits that claims 1-13 are readable on the elected species.

Respectfully submitted,

Date: January 14, 2003

Wayne L. Ellenbogen

Attorney for Applicant(s)

Reg. No. 43,602

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